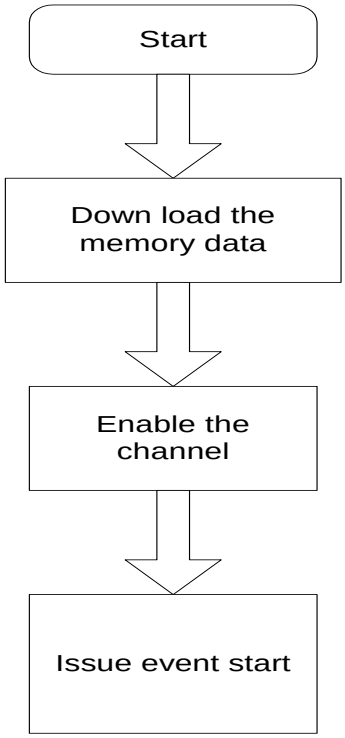
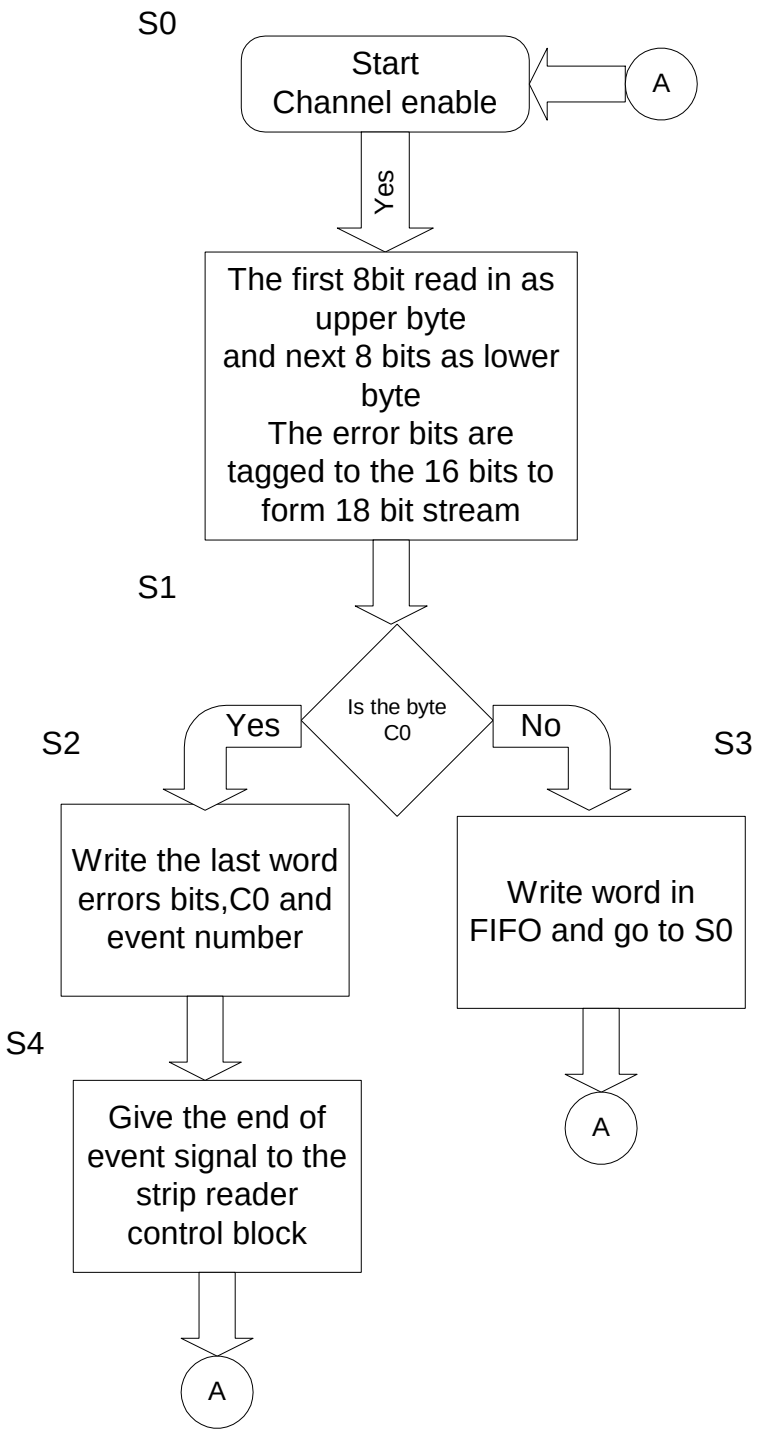
APPENDIX – A

FLOW CHARTS OF THE MODULES OF THE STC MAIN DATA PATH

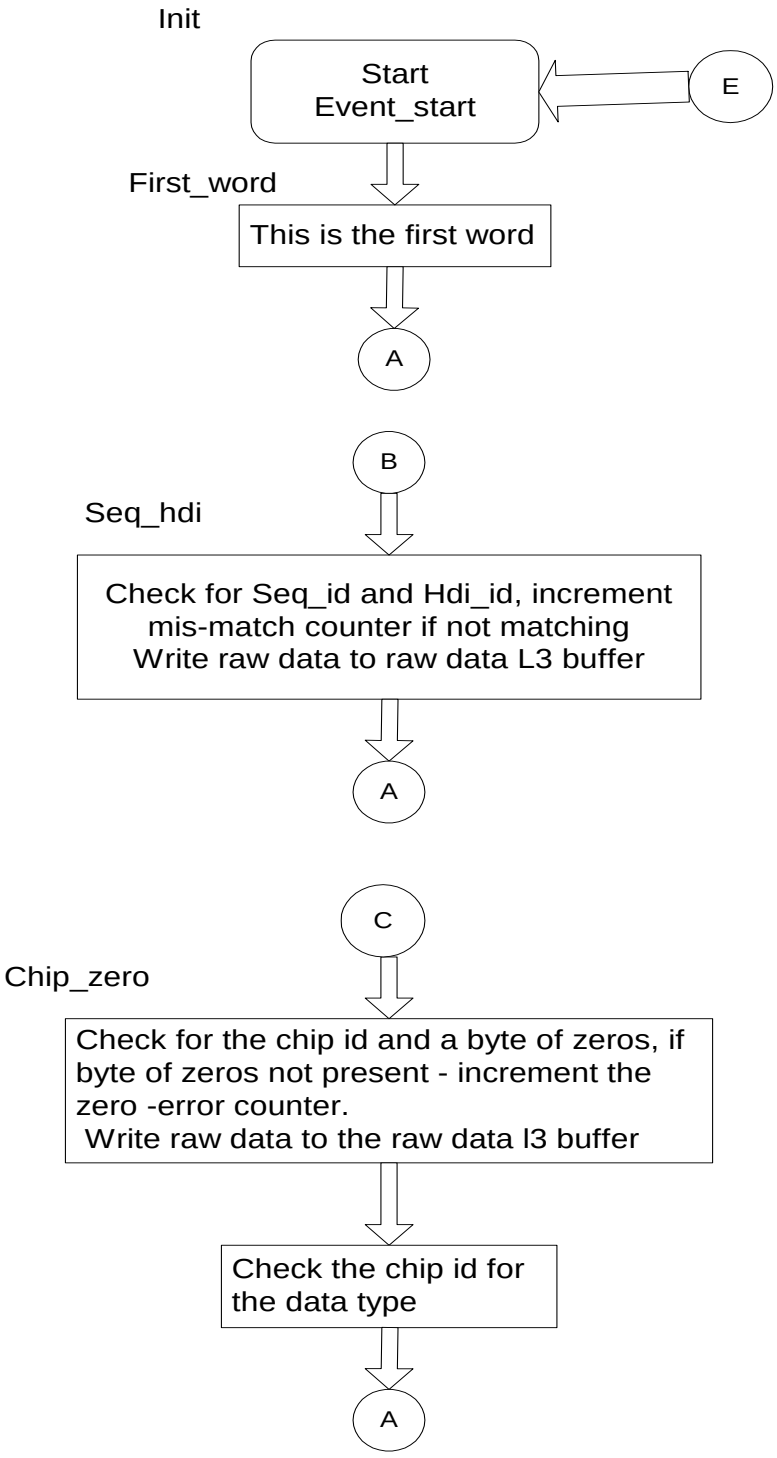
A.1 SIGNALS FROM THE MAIN CONTROL MODULE



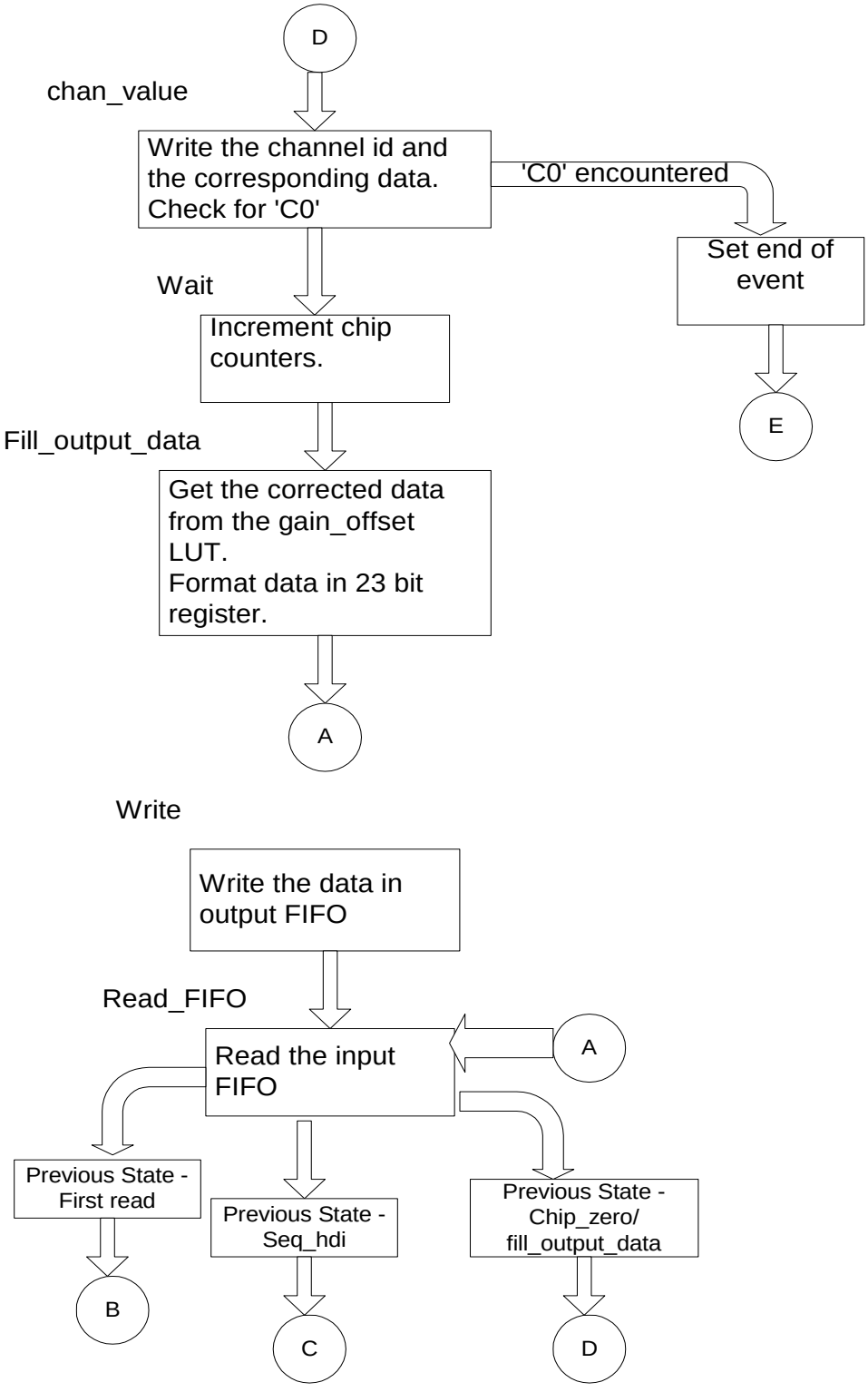
A.2 SMT DATA FILTER



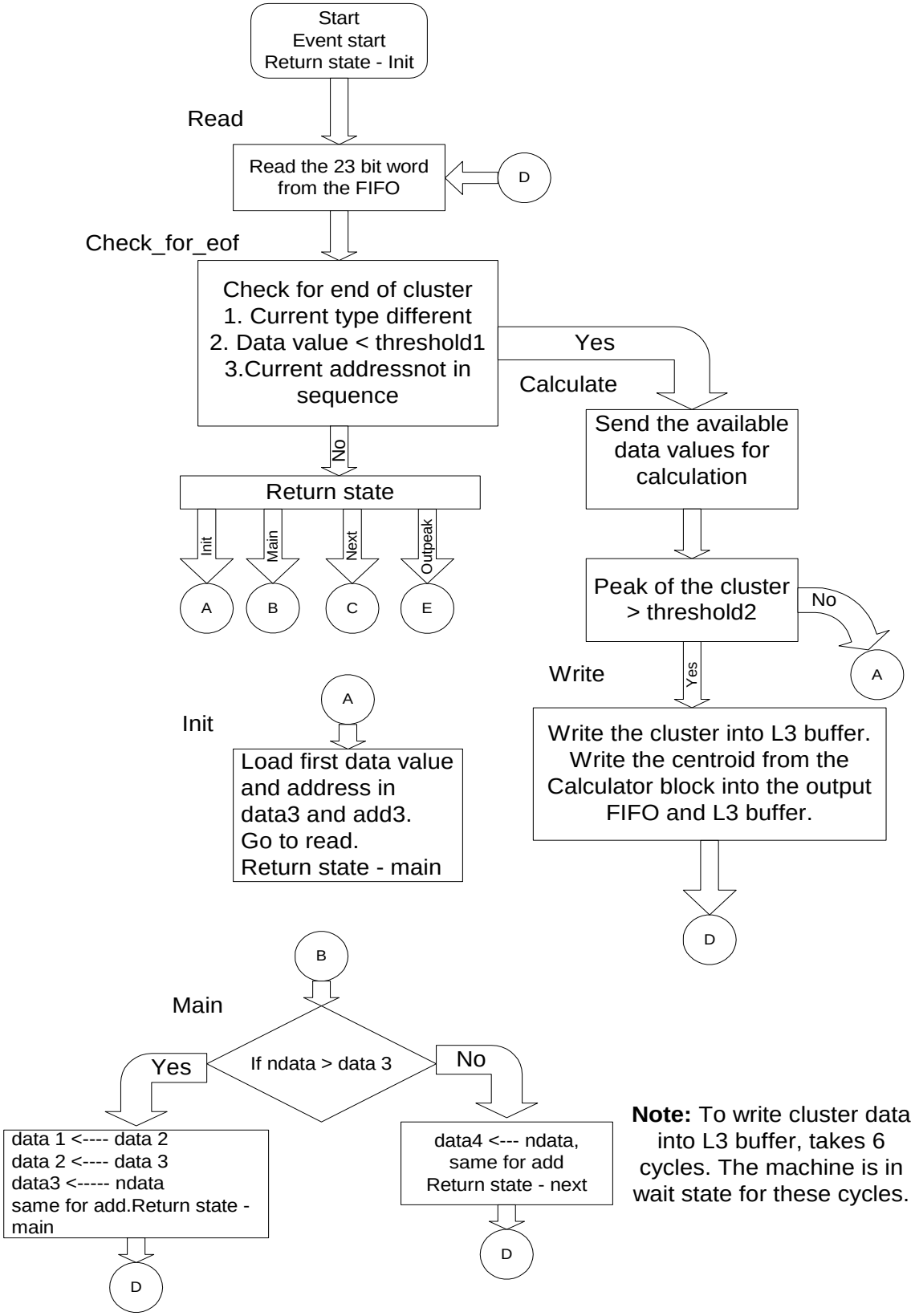
A.3 STRIP READER CONTROL



A.3 STRIP READER CONTROL (CONTINUED….)

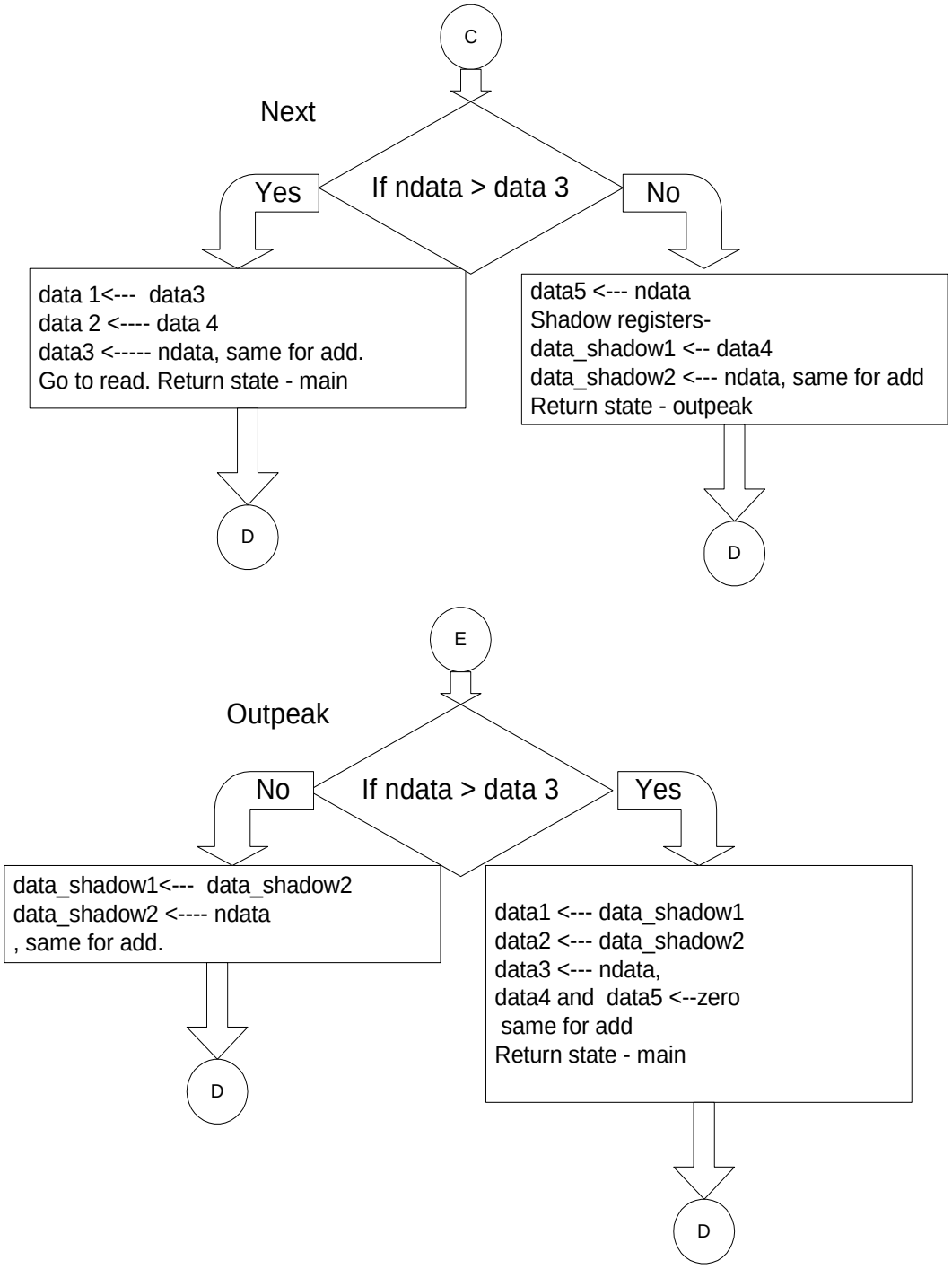


A.4 CLUSTER FINDER

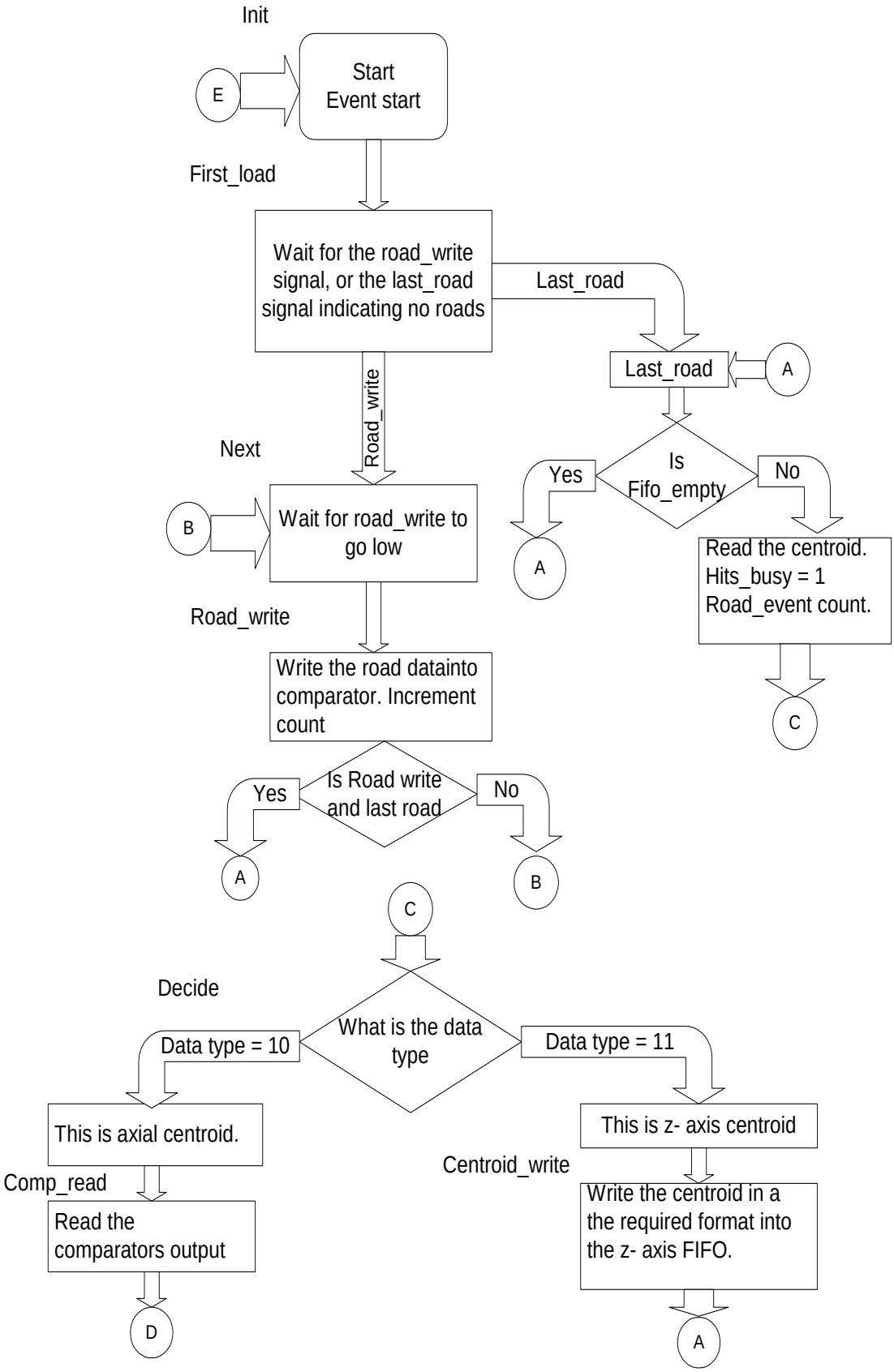


Note: To write cluster data into L3 buffer, takes 6 cycles. The machine is in wait state for these cycles.

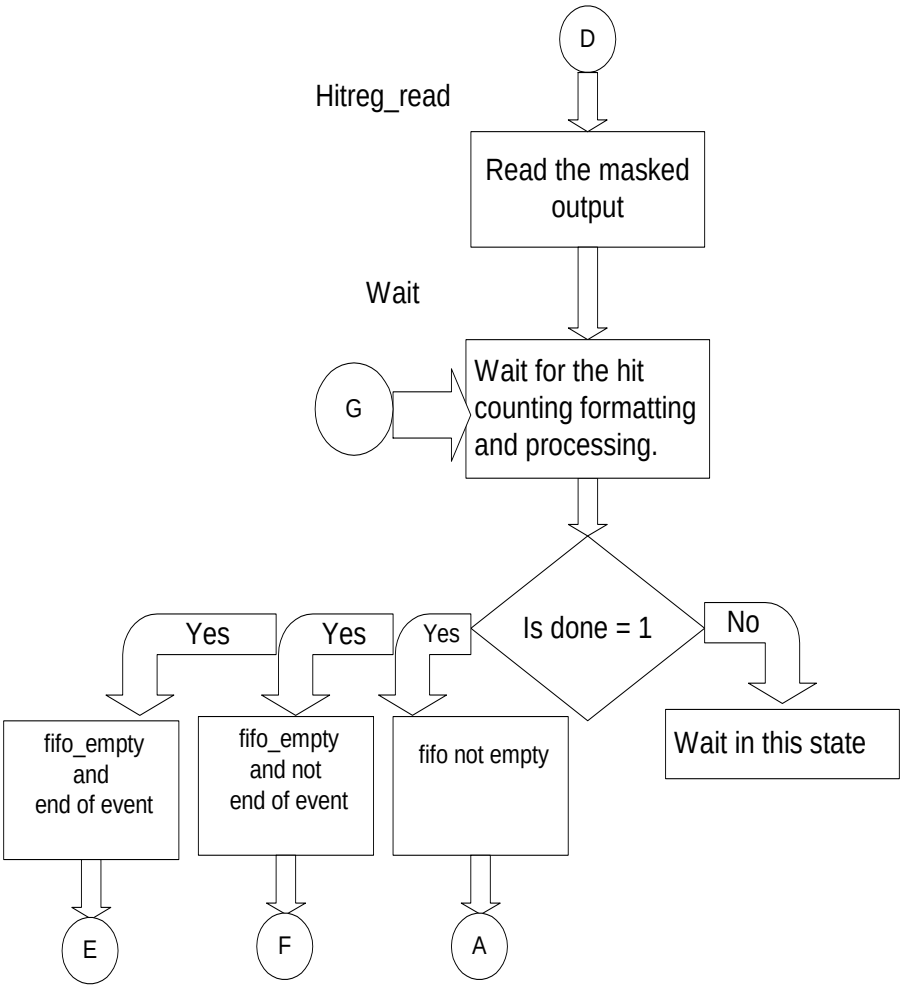
A.4 CLUSTER FINDER (continued)

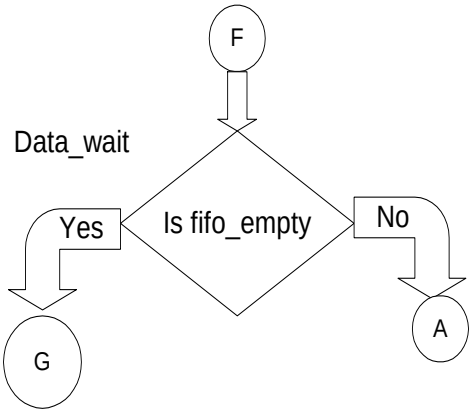


A.5 HIT FILTER – Hit filter control logic

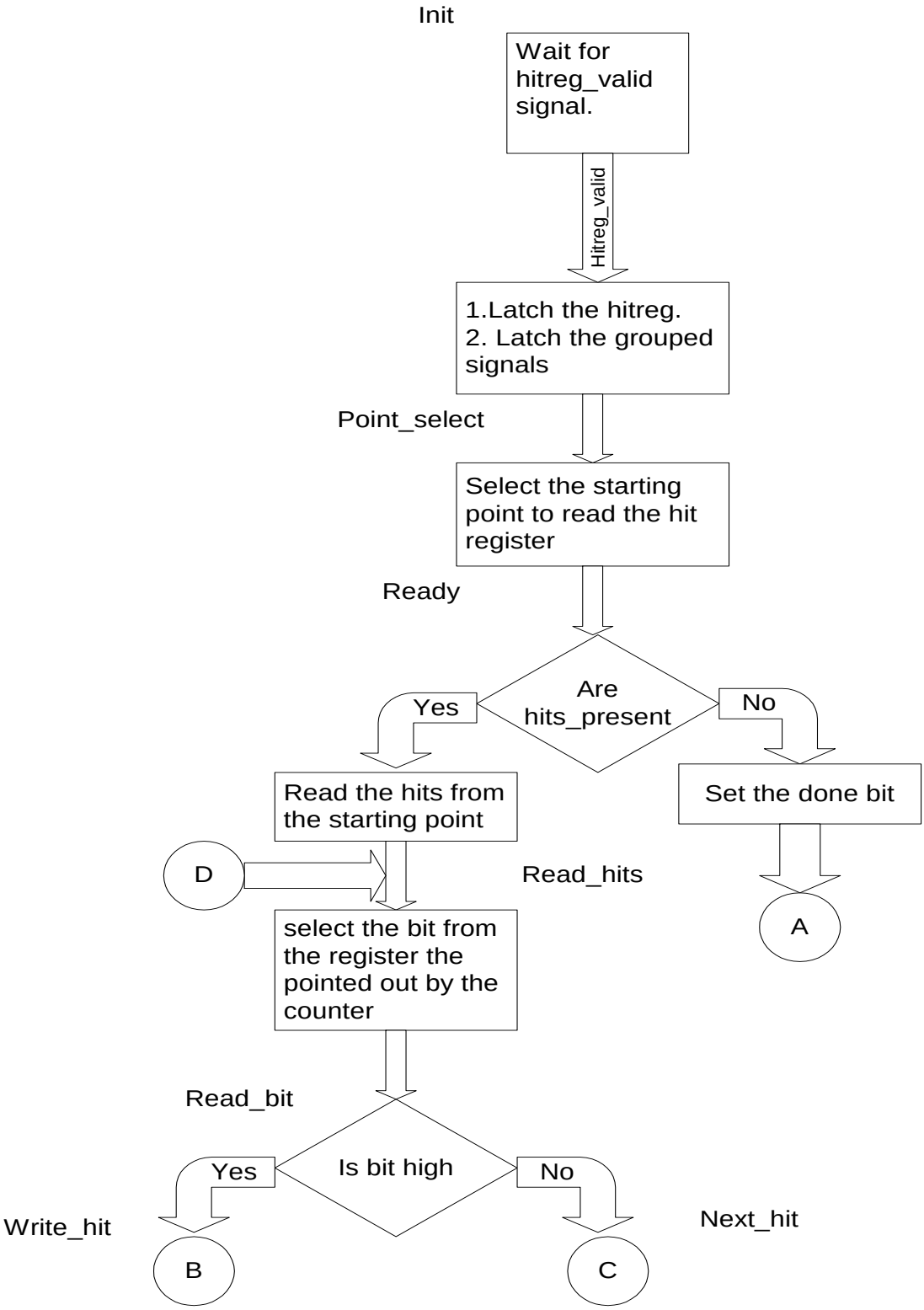


A.5 HIT FILTER – Hit filter control logic (continued)





A.6 HIT FILTER - Hit filter hit_format logic



A.6 HIT FILTER - Hit filter hit_format logic (continued)